

VK702W WIFI high-speed 24-bit data acquisition card

—Precision Accurate Speed Reliable

Description

VK702W is a wireless high-speed data acquisition card with 8-channel true differential input, 24-bit resolution, maximum sampling rate of 50KSPS, and precision pre-gain amplification. The product adopts a number of high-precision 24-bit ADC units and the pre-differential amplifier module developed by the company over the years, so that the product has the advantages of high speed, high resolution, high precision, ultra-low noise, high suppression ratio, wide measuring range and low temperature bleaching. It is suitable for various occasions of precise and high-speed acquisition.

LAN communication adopts TCP/IP and original exchange communication protocol and built-in packet loss prevention algorithm, which can ensure stable transmission and collection of data without loss for a long time.

Acquisition card extended to series version, USB VK702, LAN high-speed transmission type see VK702W.

VK702W acquisition card adopts all-metal shield, which can be used in industrial occasions with strong interference, and has the advantages of moisture-proof, shockproof and anti-interference.



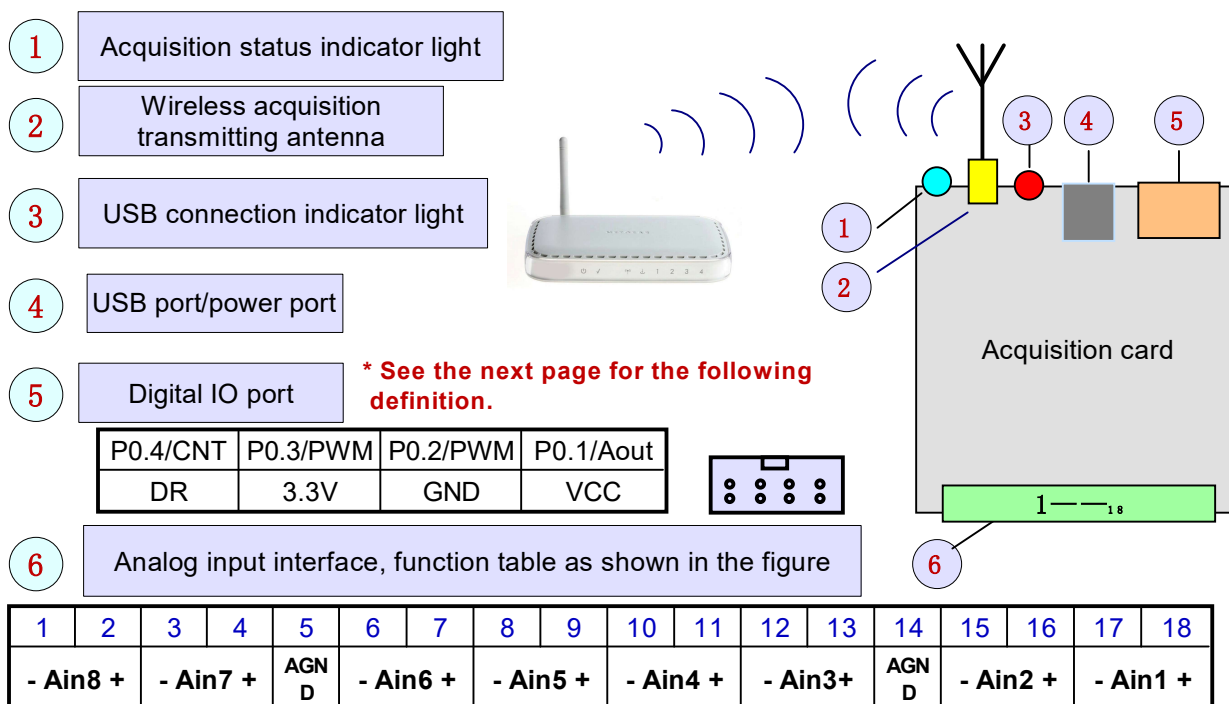
Characteristic

- High precision, high resolution: 24-bit resolution
- Ultra-low noise pre-differential amplification: Minimum measurement 50uV
- High rate synchronous acquisition: Single channel maximum 50kSPS(50k points per second), 400kps at 8 channels
- input range: 0 ~ ± 10V
- Counting/frequency measurement : Counting or frequency measurement
- Integrate 2-channel PWM output: 16 bit adjustable PWM
- Integrate 1 channel DAC output : 0 ~ 3.3 V analog
- Metal shield housing: Strong anti-interference ability

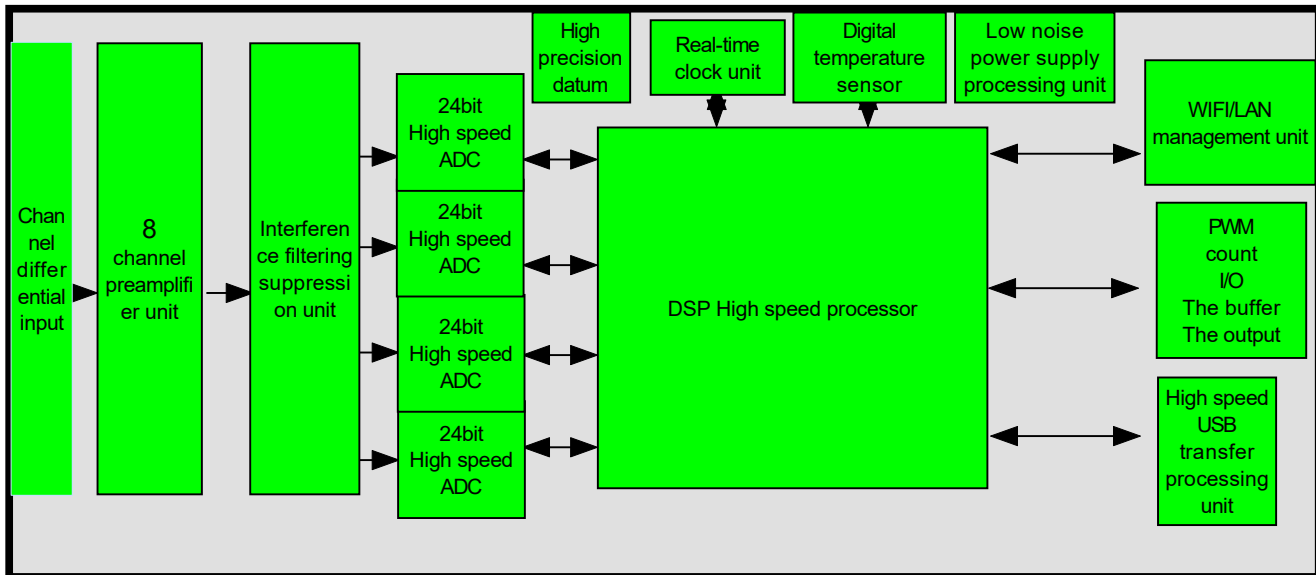
Application

- Wireless single/multi-card data acquisition
- High resolution signal measurement
- Signal trigger acquisition system
- Multi-card networking data acquisition

Acquisition card port diagram:



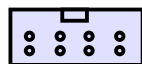
The system block diagram



Port functions

port number	name	function	note
	VCC	Digital power supply 5V, for digital power supply, can provide load current up to 50mA.	
	3.3V	Digital power supply 3.3V, for digital power supply, can provide load current up to 50mA.	
	P0.1/ Aout	<ul style="list-style-type: none"> P0.1—— Configured as a digital input/output port AOUT——Configured as an analog output port, it can output 0~3.3V 	Function 2 choose 1
	P0.2/ PWM1	<ul style="list-style-type: none"> P0.2——Configured as a digital input/output port PWM1——Configured as A PWM output port 	Function 2 choose 1
	P0.3/ PWM2	<ul style="list-style-type: none"> P0.3——Configured as a digital input/output port PWM2——Configured as A PWM output port 	Function 2 choose 1
	P0.4/ CNT	<ul style="list-style-type: none"> P0.4——Configured as a digital input/output port CNT——Configured as a counter/frequency meter input port Ext Trig——Configure external trigger acquisition 	Function 3 choose 1
	DR	Built-in router reset pin	
	DGND	Digital earth	
1,3,6,8,10,12,15,17	AinX+	Analog input negative input terminal	If the ground wire is connected to the digital terminal during the analog terminal acquisition, digital interference may be introduced to reduce the acquisition accuracy.
2,4,7,9,11,13,16,18	AinX-	Simulate the positive input terminal	
5,14	AGND	Analog ground	

The digital IO port leads to the definition behind the wire.



DR	P0.4	3.3V	P0.3	GND	P0.2	VCC	P0.1
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VK702W wireless data acquisition card

Integrated electrical parameters

Project	Unit	Typical	Range
USB supply voltage:	V	5	4.5~5.5
USB power current:	mA	450	400~500
ADC analog port input voltage	V		+~10
Digital port input VL low level	V	0	-0.3~1
Digital port input VH high level	V	3.3	2~5.5
Digital port output voltage	V	3.3	3.2~3.4
Digital port output drive current (high current output)	mA	10	
Digital port input absorption current (5V input voltage)	uA	170	
ADC maximum sampling rate	ksps		50
Minimum resolution voltage (input range selected -1mV~+1mV)	uV		10
Aout output voltage	V		0~3.3
PWM output frequency (P0.2/PWM1, P0.3/PWM2 ports)	Hz		0~100k
PWM duty ratio (P0.2/PWM1, P0.3/PWM2 ports)	%		0~100
Counter input maximum frequency (P0.4/CNT port)	Hz		100K
The counter enters the maximum value		2^{64} joules	
Working temperature:	c		-20~ 85
Storage temperature	c		-40~ 105
Physical size (length, width and height)	mm	110*82*24	Connector length is not included.

Use absolute maximum values safely

Project	unit		*Exceeding the absolute maximum may damage the device and cause irreparable damage.
USB supply voltage:	V	-1~+6	
ADC analog port	V	+200 (There are protective circuits inside.)	
Digital ports	V	+200 (There are protective circuits inside.)	
DAC output port	V	-1~+6	
All Port Electrostatic Input (ESD)	V	2000	

● ADC emulates the conversion unit

ADC input detailed electrical parameters			
Project	Unit	Typical	Note
Differential input common mode rejection ratio(CMRR)	dB	130	
Input bias current	nA	1	
Input bias voltage	uv	10	
Input equivalent voltage noise	nVp-p	200	When the input range is +-10V, the maximum value is 400.
Input equivalent current noise	pAp-p	1	The maximum value is 2.
Equivalent input capacitance	pF	400	
Input resistance	GΩ	1	
The overall maximum temperature drift of the ADC amplification acquisition unit	ppm/° C	6	

ADC sampling true valid values vs input ranges			
Program set	Corresponding measuring range	Minimum resolution	Note
0	-10V~+10V	0.2mV	
1	-5V~+5V	0.1mV	
2	-2.5V~+2.5V	0.05mV	
3	-1V~+1V	0.02mV	
4	-500mV~+500mV	0.01mV	

Precautions for Use

The ground wire is divided into digital ground and analog ground.

The port on the USB power side is the digital terminal (including Aout output). On the other side is the analog input port.

When in use, digital input and AOUT output should be used in conjunction with digital ground (DGND), while ADC acquisition terminal should be used in conjunction with analog ground (AGND), so as to avoid digital interference of digital input to analog input.

DGND Digital earth

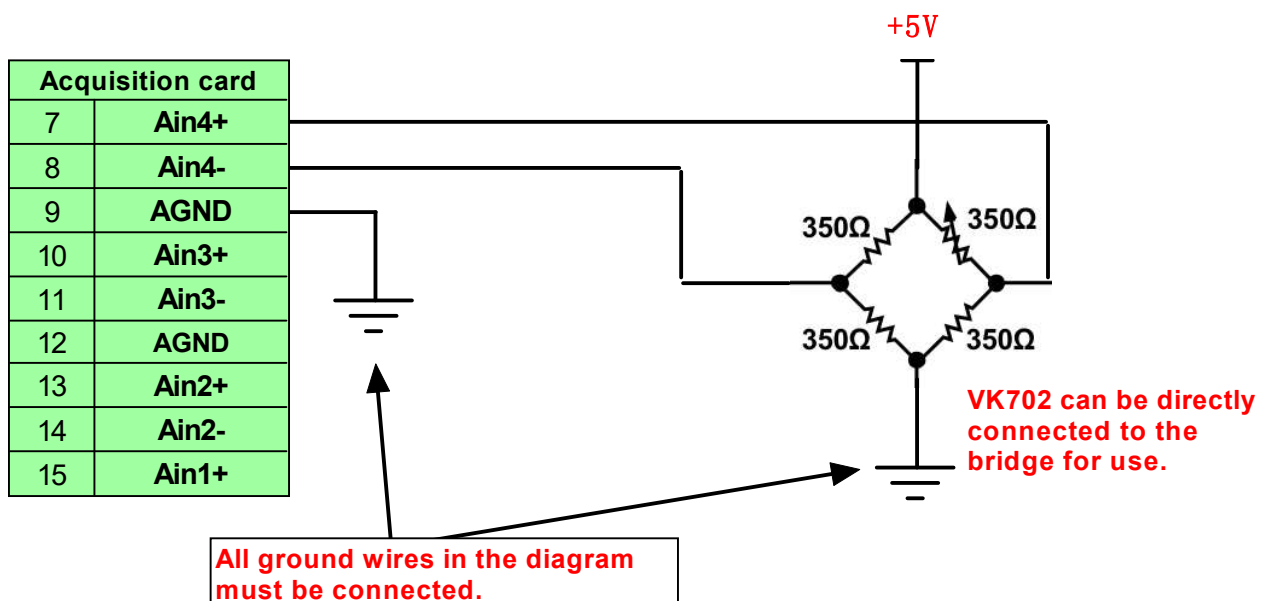
VCC	DGND	3.3V	DR
P0.1/Aout	P0.2/PWM	P0.3/PWM	P0.4/CNT

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18
- Ain8 +	- Ain7 +	AGND	- Ain6 +	- Ain5 +	- Ain4 +	- Ain3 +	AGND	- Ain2 +	- Ain1 +								

AGND Analog ground

Differential mode of ADC input

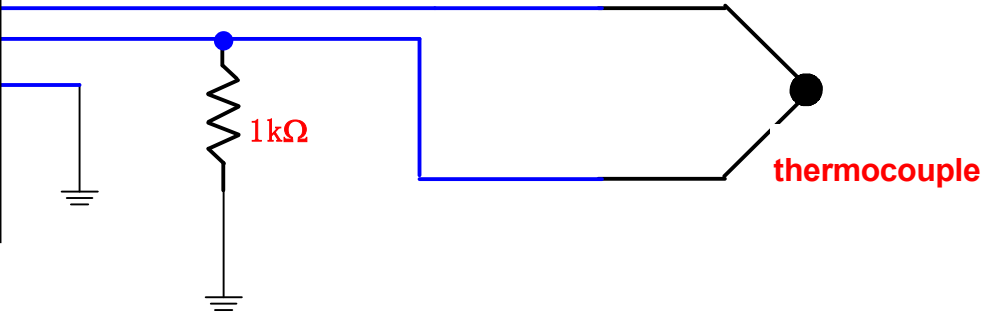
Differential mode is the least noise input mode, which can effectively suppress all kinds of common mode interference. But care must be taken to provide the correct input loop to the input end. The correct common ground is the first step in ensuring the input loop.



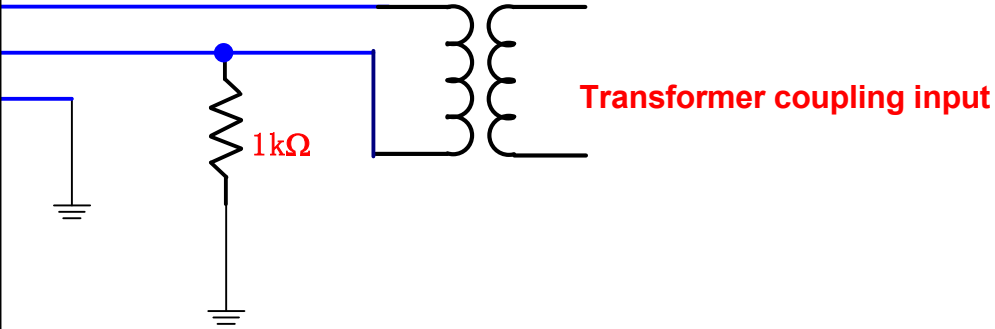
Differential mode of ADC input

If there is no common ground on the input side, you can create the input return path as follows.

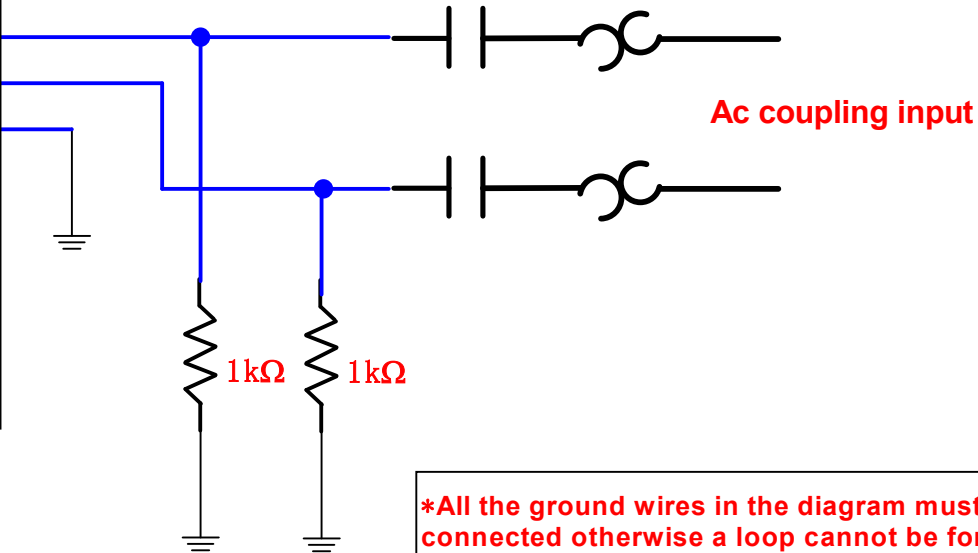
Acquisition card	
7	Ain4+
8	Ain4-
9	AGND
10	Ain3+
11	Ain3-
12	AGND
13	Ain2+
14	Ain2-
15	Ain1+



Acquisition card	
7	Ain4+
8	Ain4-
9	AGND
10	Ain3+
11	Ain3-
12	AGND
13	Ain2+
14	Ain2-
15	Ain1+



Acquisition card	
7	Ain4+
8	Ain4-
9	AGND
10	Ain3+
11	Ain3-
12	AGND
13	Ain2+
14	Ain2-
15	Ain1+

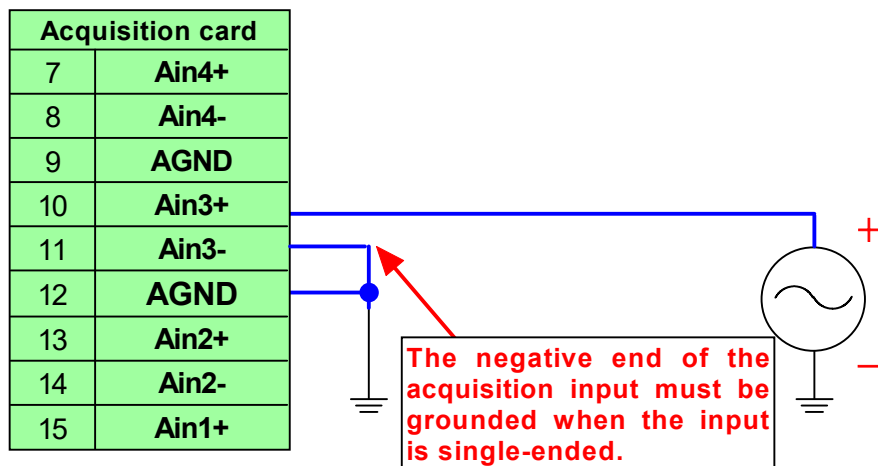


*All the ground wires in the diagram must be connected otherwise a loop cannot be formed.

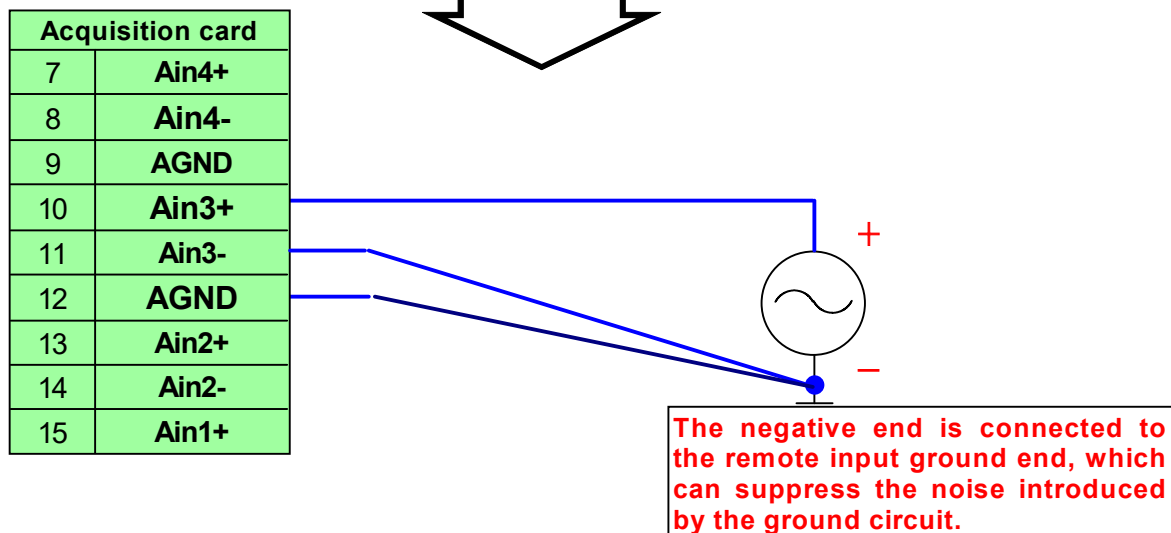
Application of single-ended input to ADC input

When the input is single, the negative end of the differential input must be grounded.

When the acquisition card is used to collect non-differential signals, it can also give full play to the performance of high suppression ratio and eliminate the noise caused by ground wire.



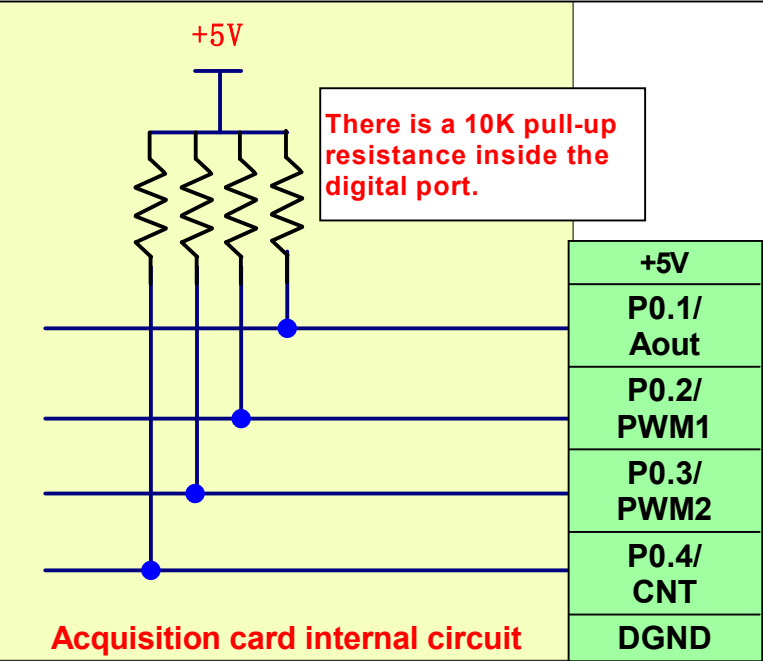
Better
connection



*All the ground wires in the diagram must be connected otherwise a loop cannot be formed.

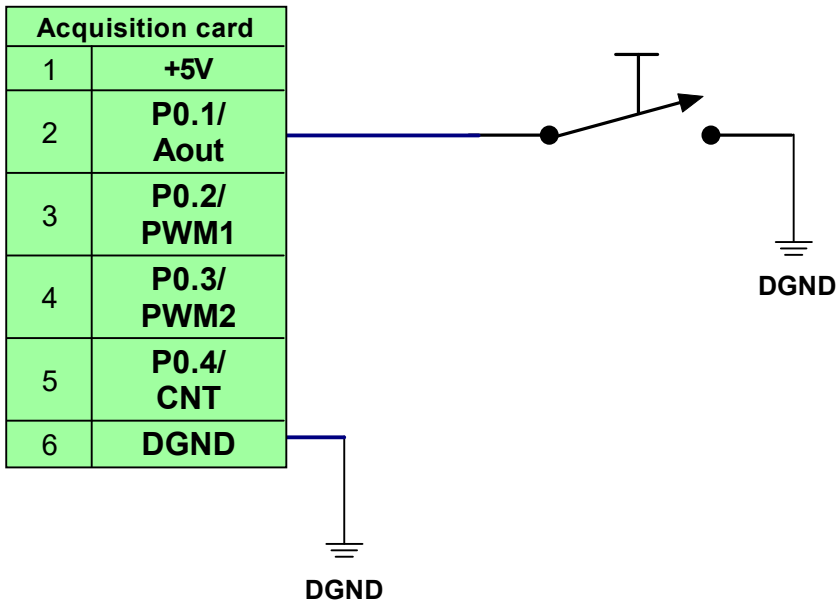
Digital port as input application

When the acquisition card is used as input, its internal pull-up resistance can be more convenient to use.



Key in to make

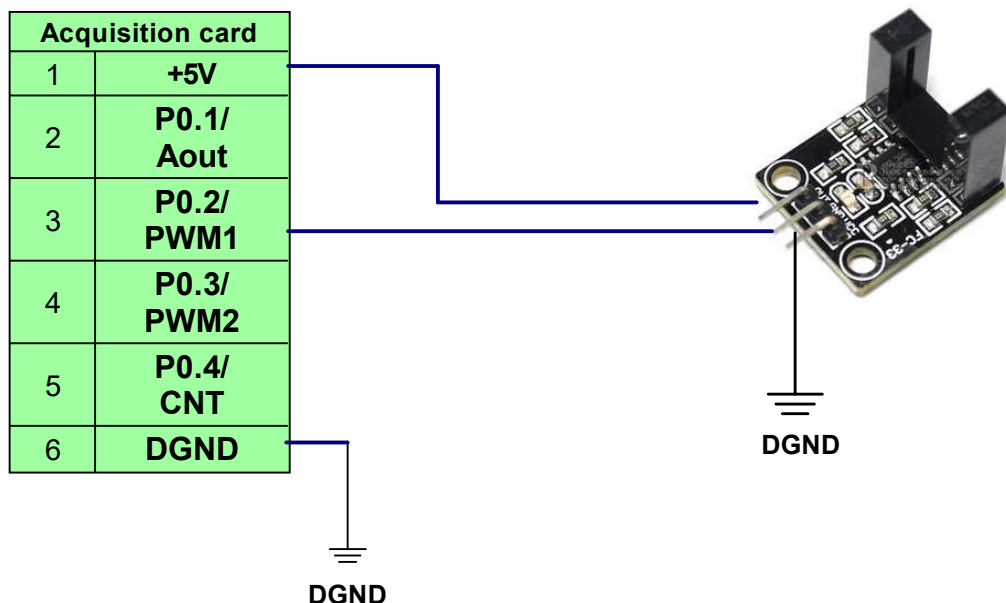
When the acquisition card is used as input, its internal pull-up resistance can be more convenient to use.



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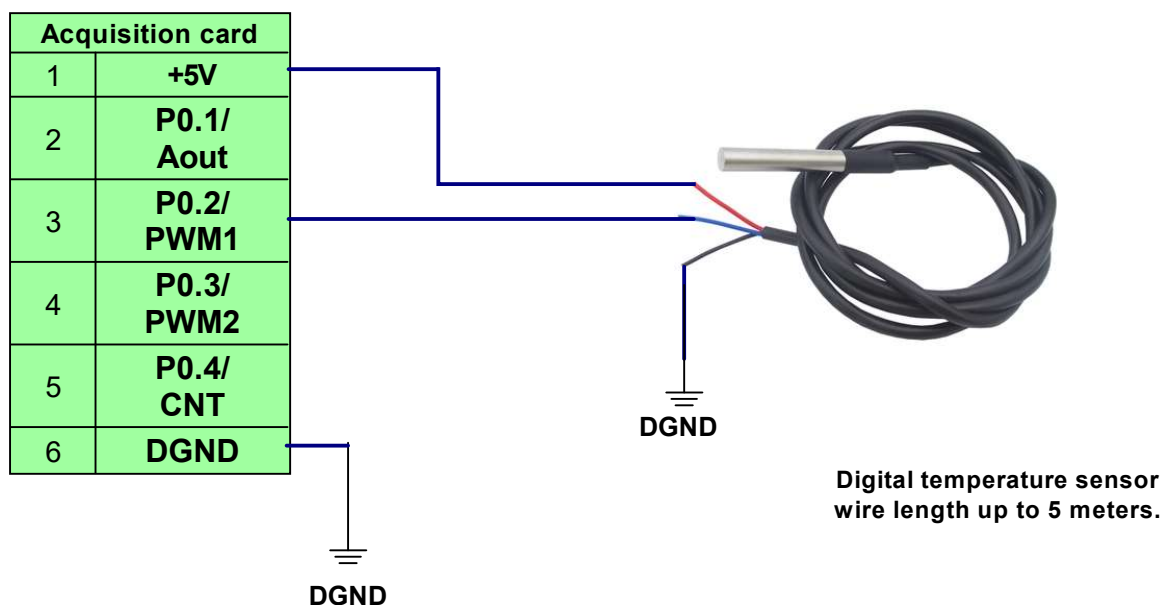
Use an internal 5V power supply to supply the sensor input

When the acquisition card is used as input, its internal pull-up resistance can be more convenient to use.



Any digital port can be connected to digital temperature sensor

Any digital port of the acquisition card can be connected with 18B20 digital temperature sensor to directly read the temperature value.



LED light status and indication

Power status indicator (beside USB interface)

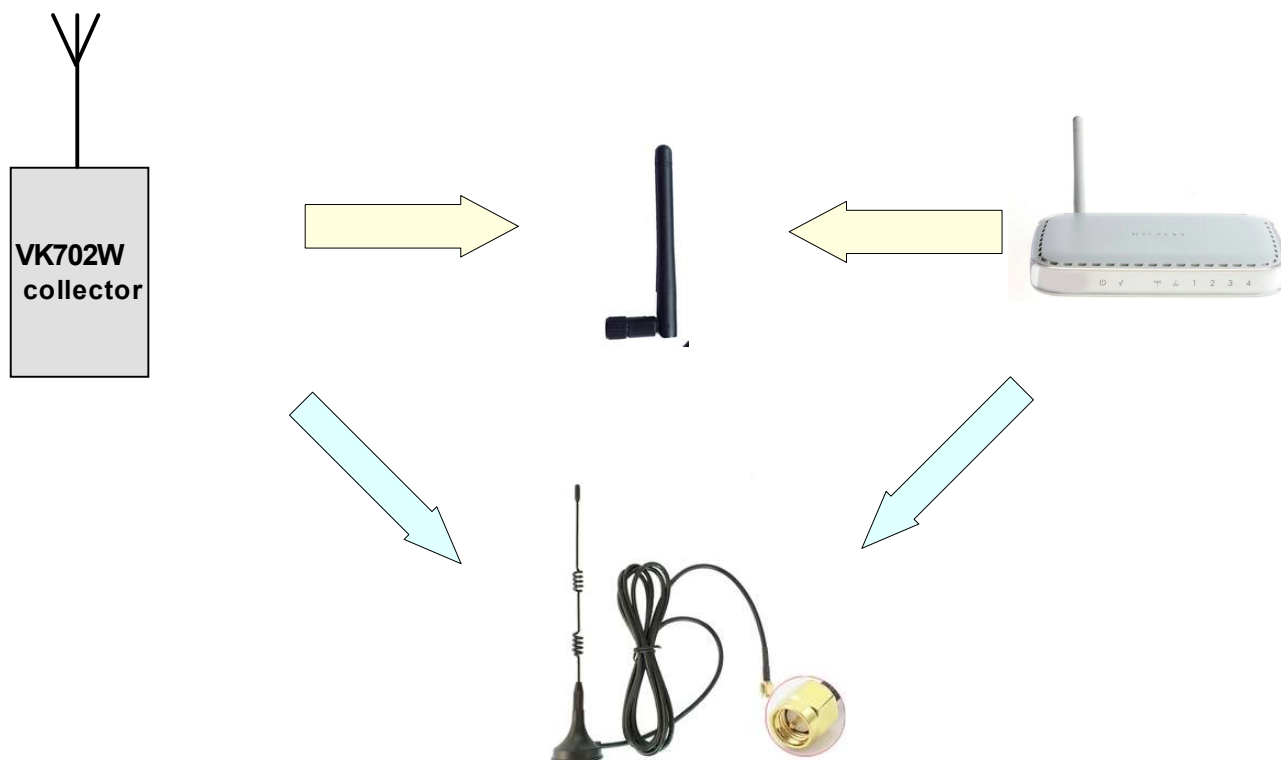
Network interface status indicator

	bright	destroy	flashing
Green light	Data not sent	normal	
Yellow light	Data in transit	No transmission	Data in transit

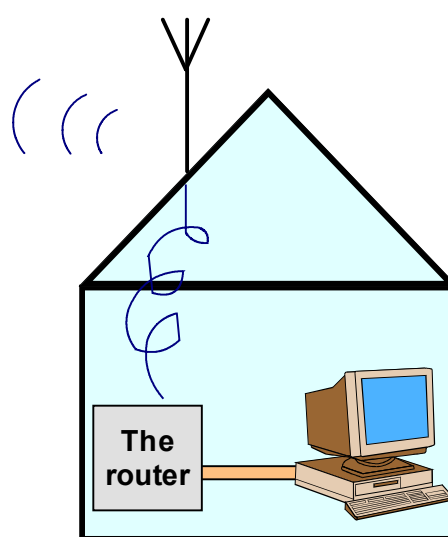
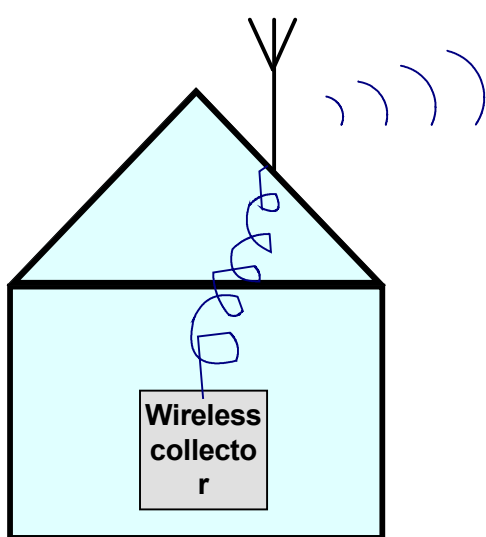
VK702W wireless data acquisition card

Selection of antenna for wireless transmission

If the wireless environment is good, a 3.5dB gain antenna with a length of 10CM can be provided by default. If the environment is not ideal or the transmission distance is longer, we can choose another high gain 7dBI omnidirectional antenna. Or users can make their own WIFI antenna, easy to lead to more convenient transmission and reception of the open.

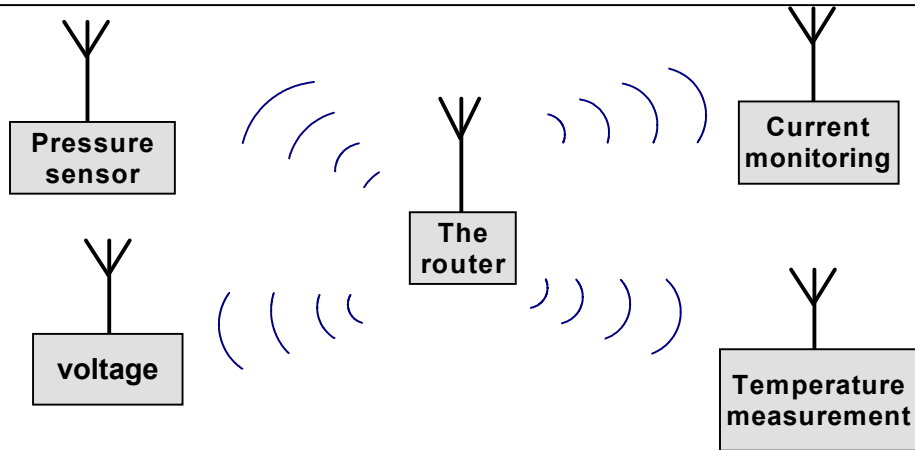


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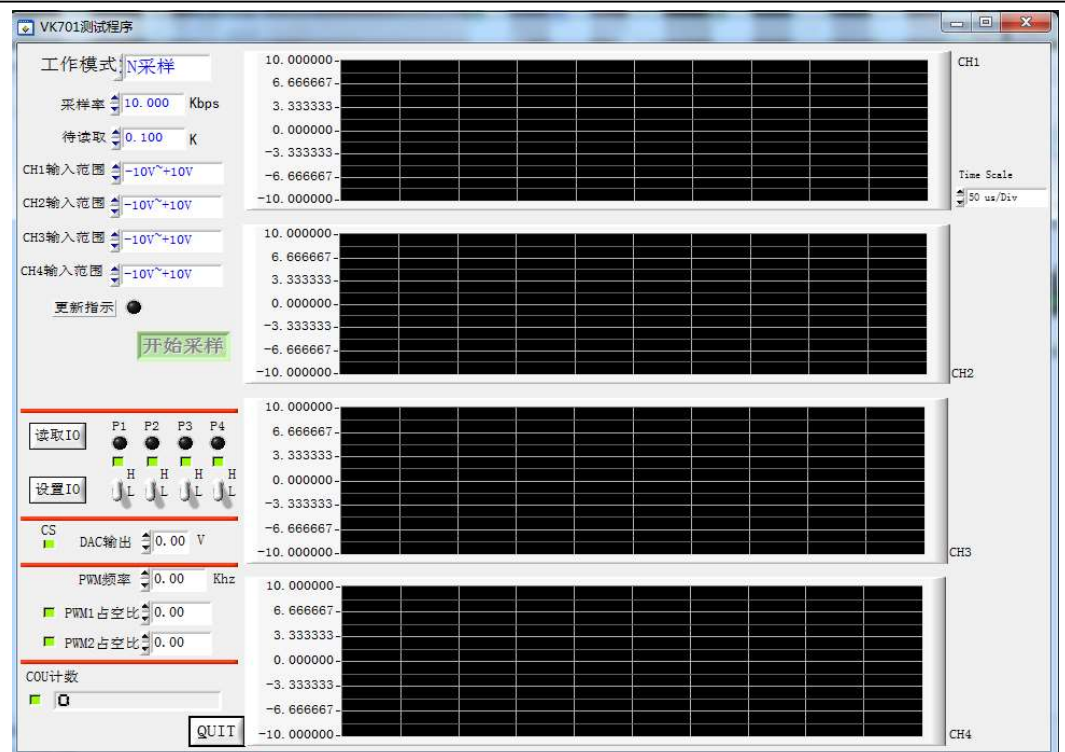
Multiple wireless front-end acquisition can be done simultaneously

This collection system supports a collection terminal to carry out time-sharing collection for multiple collection front ends, which can easily build a multi-point measurement and monitoring system. A single receiver can support up to 255 collection front ends, but the rate and transmission efficiency will decrease with the increase of the number. If the system has a higher requirement on the rate and a better transmission environment, we can choose our WIFI transmission version of wireless acquisition scheme.



PC software

The wireless collector VK702W can be independently powered, and it adopts TCP/IP protocol to exchange data of various receiving terminals through wireless WIFI data transmission.



Built-in wireless routing unit parameters

project		*
Wireless protocol	IEEE802.11 b/g/n	
Wireless rate	Maximum 150 MBPS	
Digital ports	+200 (There are protective circuits inside.)	
RF power	Maximum 18dbm	
Wireless transmission distance	100 m (Open field)	

Physical dimension drawing

